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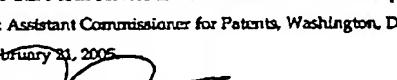
Applicant: Chambers Art Unit: 2818
Serial No.: 10/696,539 Examiner: Le, Dung Anh
Filing Date: 10/29/2003 Docket No.: TI-36031
Customer No.: 23494 Conf. No.: 7816
Title: METHODS FOR FABRICATING A TRIPLE-GATE MOSFET
TRANSISTOR

Ex Parte Quayle Amendment

Assistant Commissioner of Patents
Washington, DC 20231
February 21, 2005

Dear Sirs

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)
I hereby certify that this correspondence is being deposited with
the U.S. Postal Service as First Class Mail in an envelope addressed
to: Assistant Commissioner for Patents, Washington, D.C. 20231 on
February 21, 2005.



Jacqueline J. Garner, Reg. No. 36,144

The following amendments and remarks are offered in response to the Examiner's Office Action dated 12/22/2004. They are respectfully submitted as a full and complete response to that Action.

Please amend the above-referenced application as follows:

The Amendments to the Specification begin on page 2.

The Amendments to the Abstract begin on page 3.

Amendments to the Claims are reflected in the listing of claims that begin on page 4 of this paper.

There are no Amendments to the Drawings.

The Remarks begin on page 7.